

## ABSTRACT

Symbols converted by a serial-to-parallel converting part (12) from serial to parallel form are subjected to inverse Fourier transform processing in an inverse Fourier transform part (13) as sub-carrier signal components of a frequency interval equal to the symbol rate, then the levels of the resulting time domain signal components are compared with a permissible peak level  $C_{th}$  in a peak component detecting part (22) to detect peak components, and the peak components are transformed by a Fourier transform part (23) to frequency domain components, which are subtracted by subtractors (24<sub>0</sub> to 24<sub>N-1</sub>) from the corresponding sub-carrier signal components to thereby reduce the peak power.

15

20

25